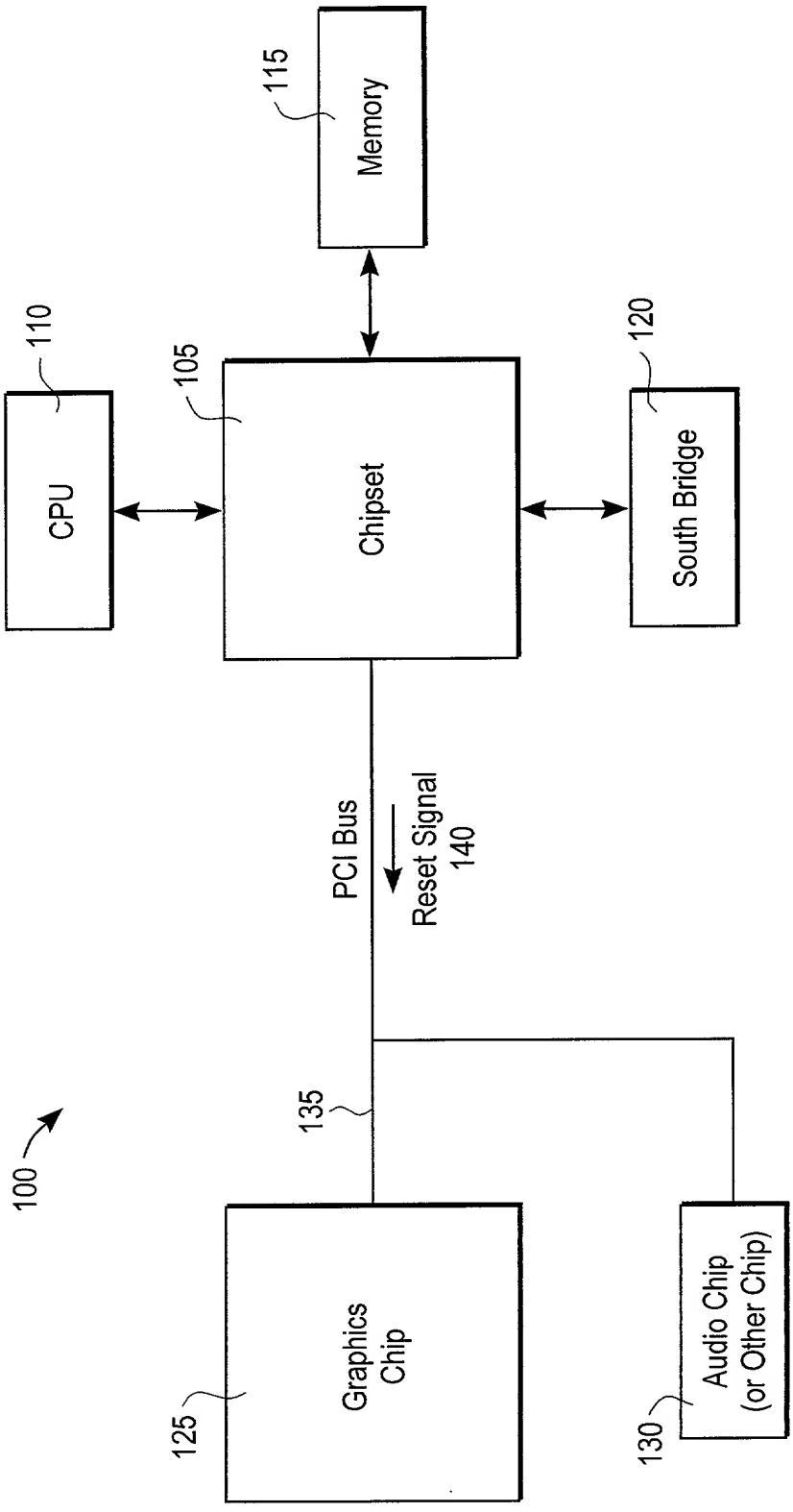


FIG. 1 (PRIOR ART)

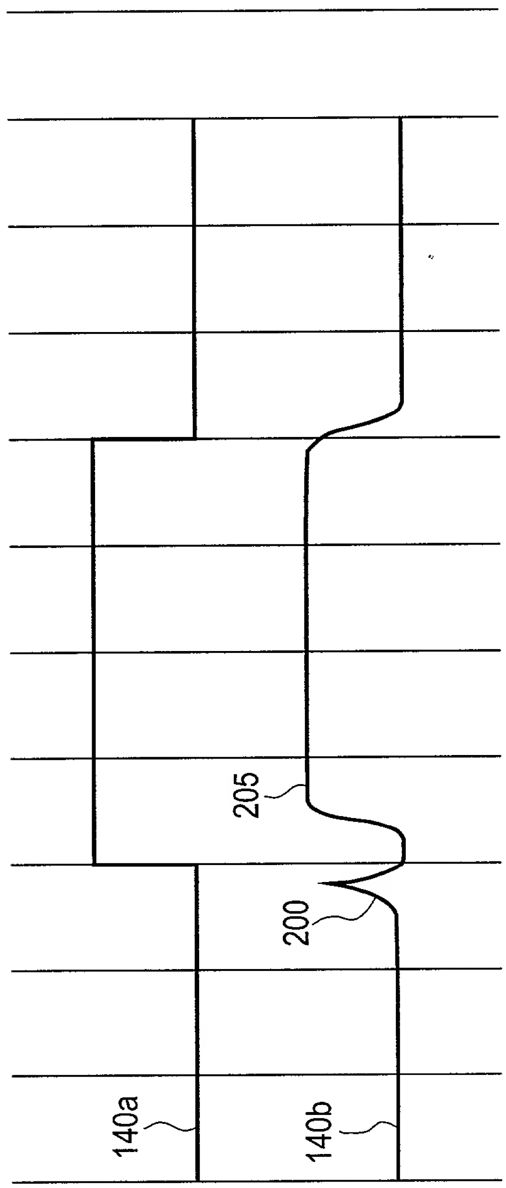


FIG. 2

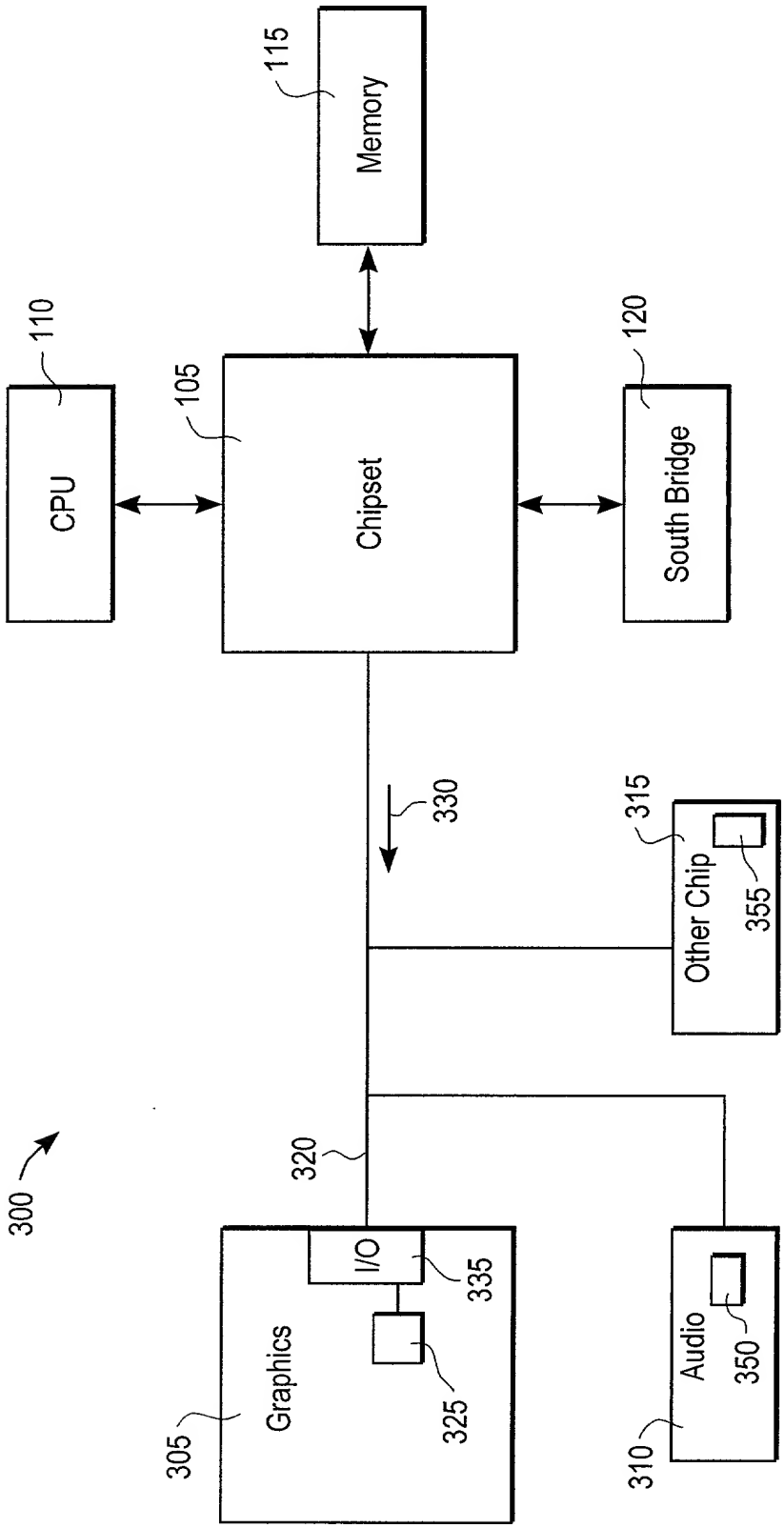


FIG. 3

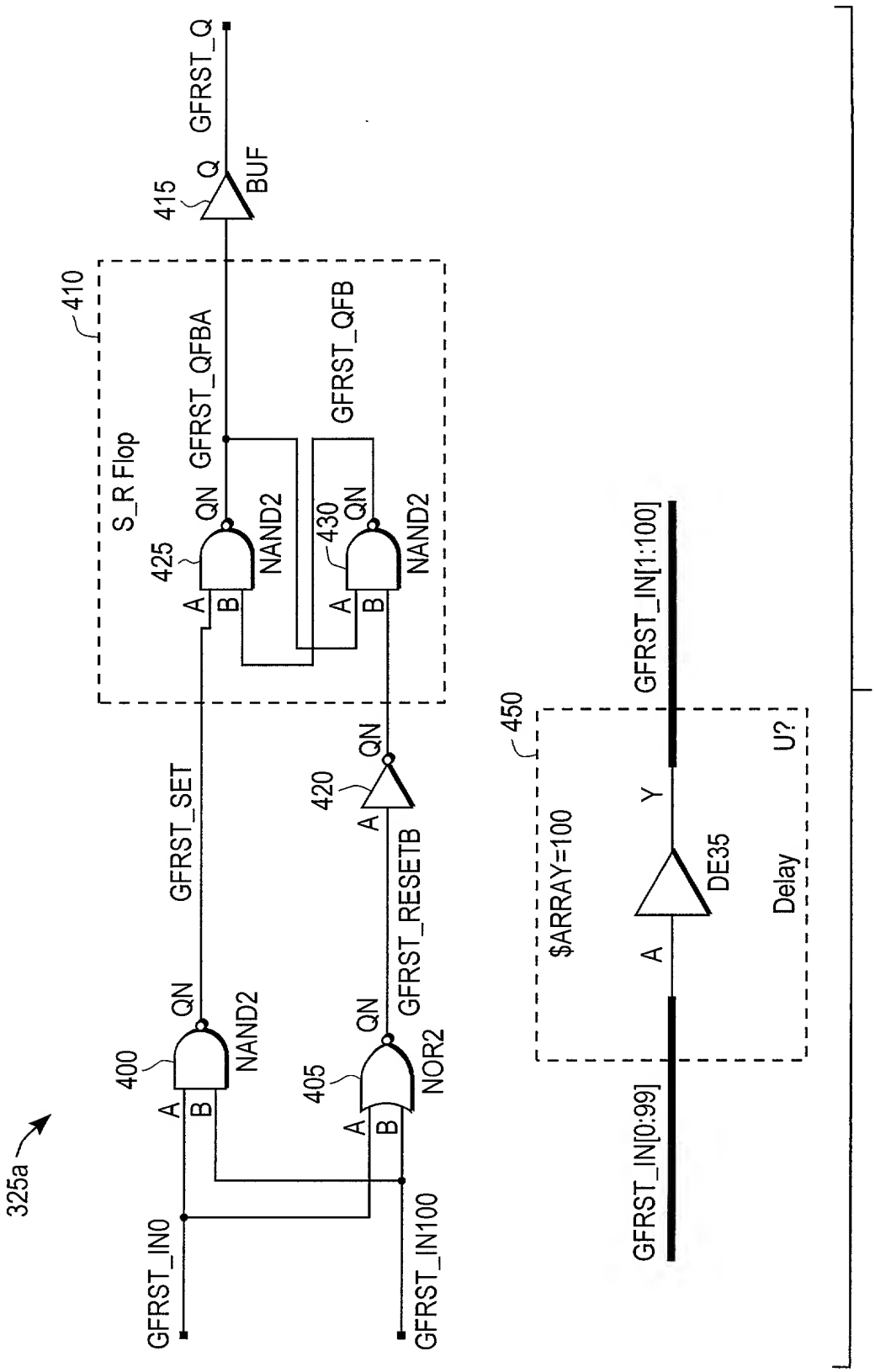


FIG. 4

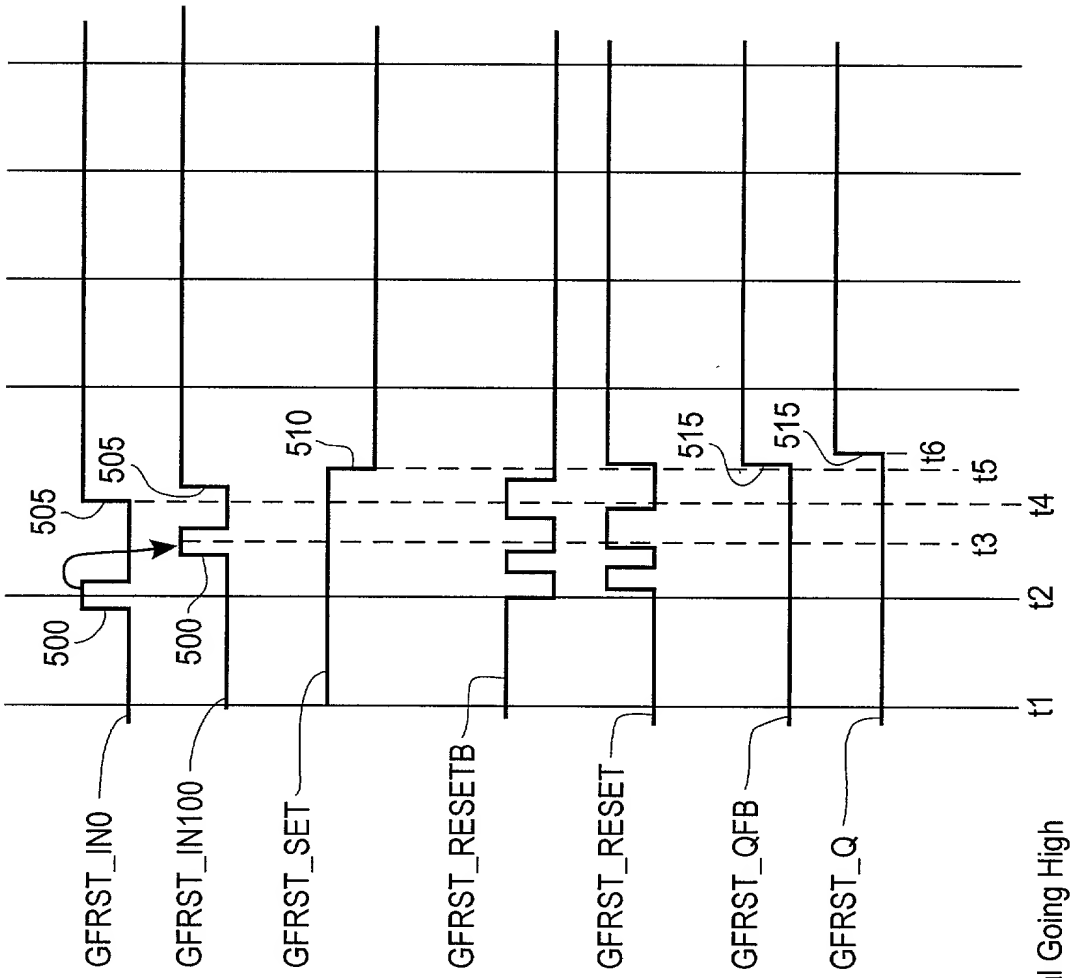


FIG. 5

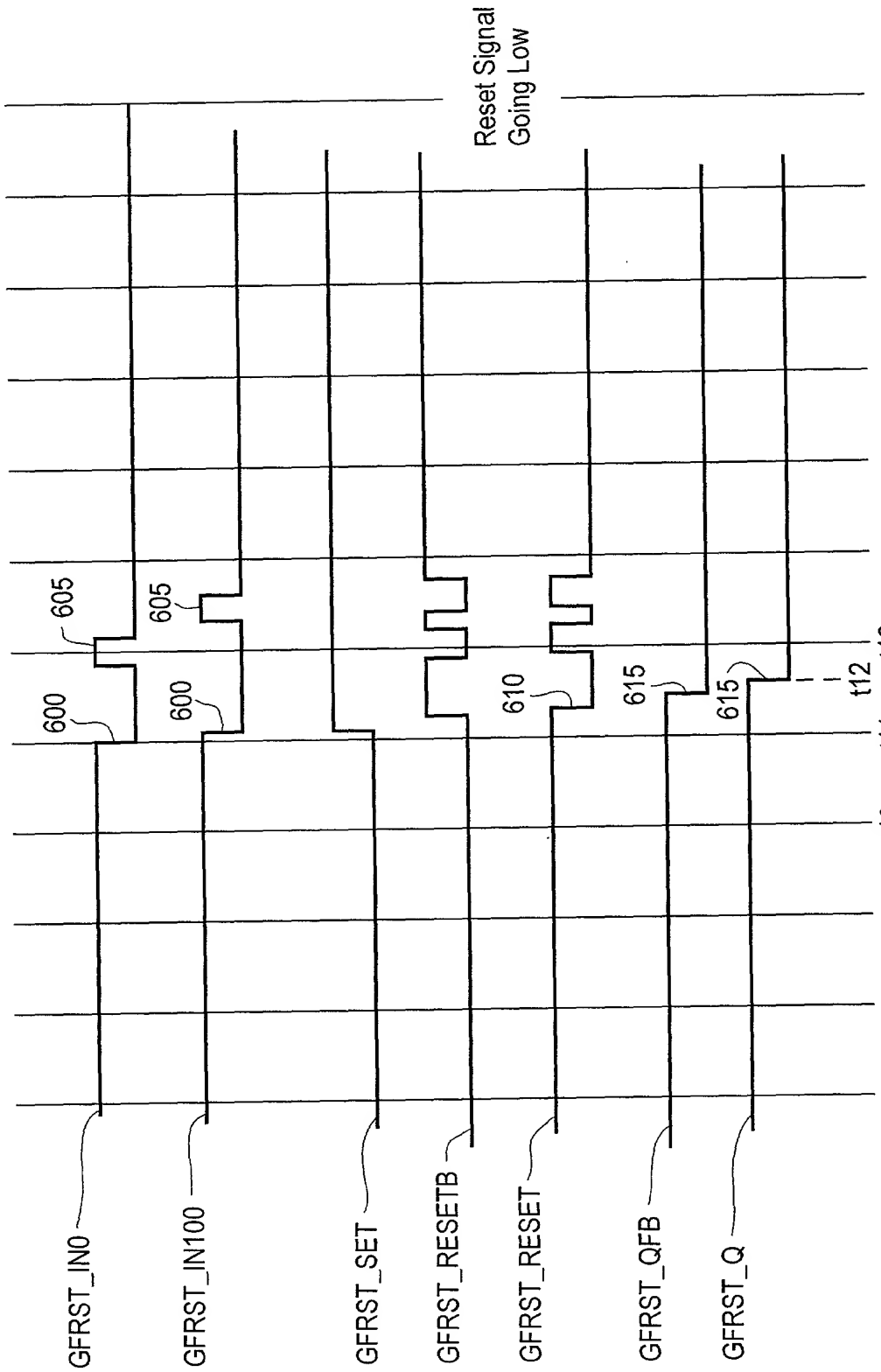


FIG. 6

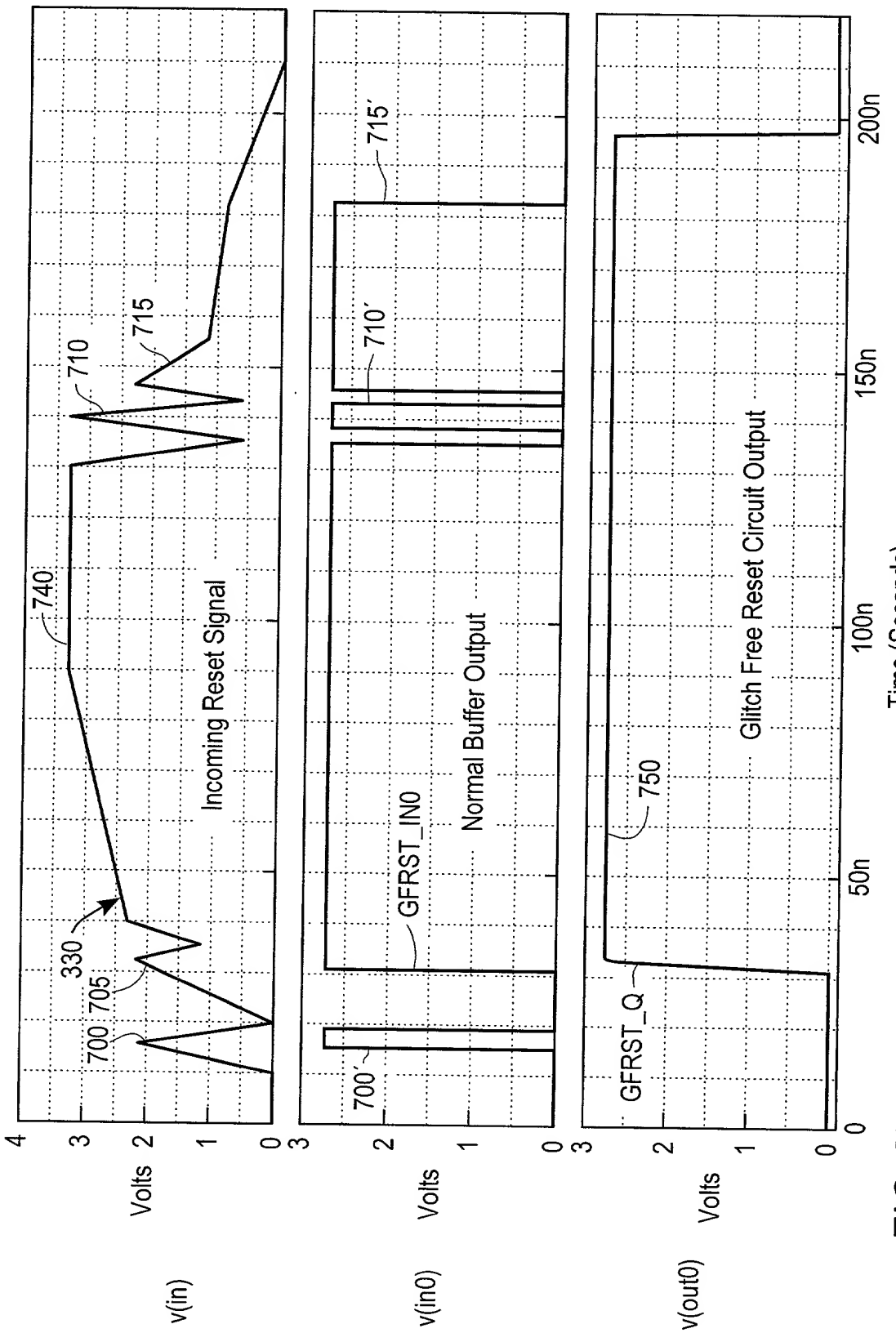
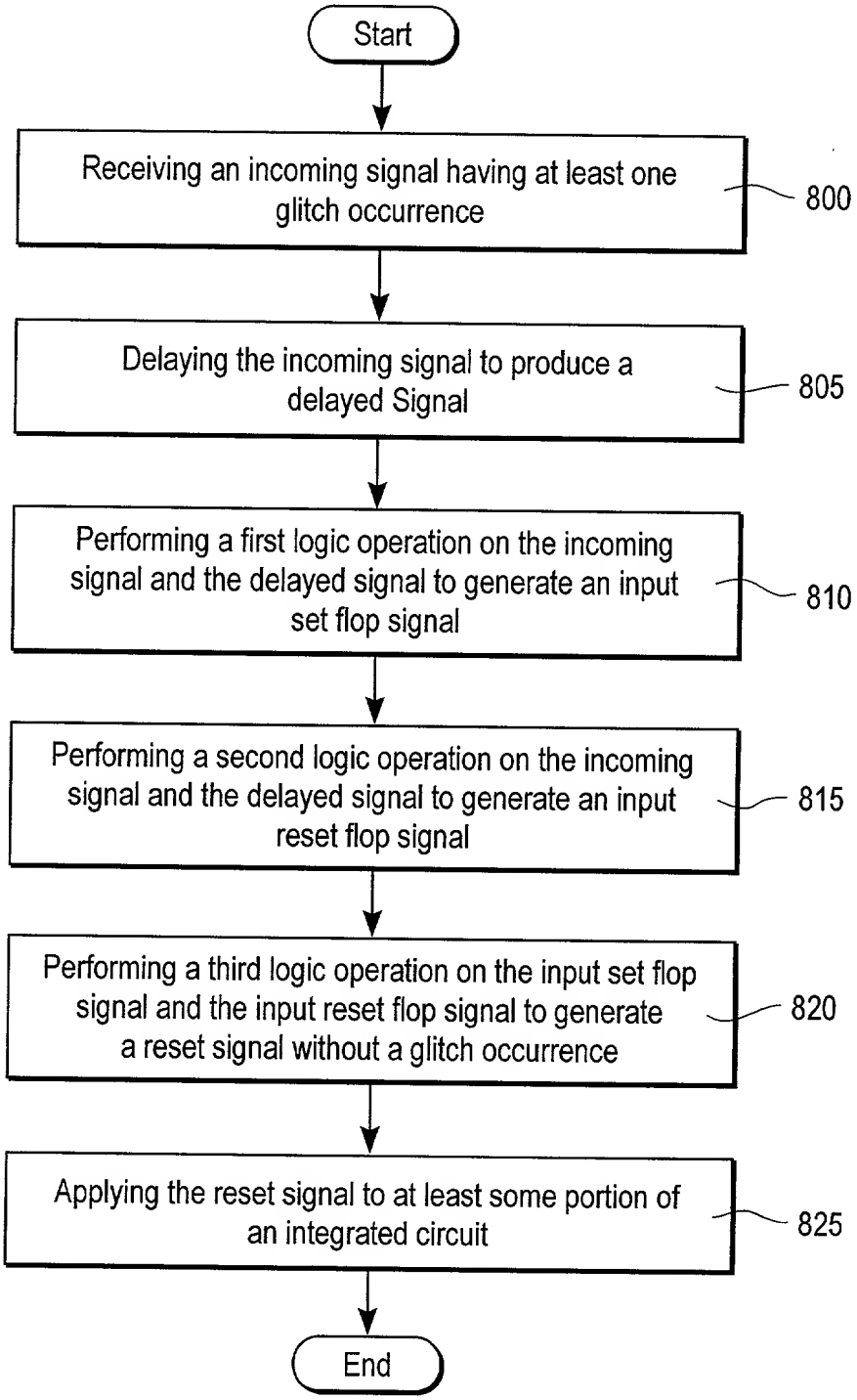


FIG. 7



Method of Compensating for Glitch Occurrence

FIG. 8

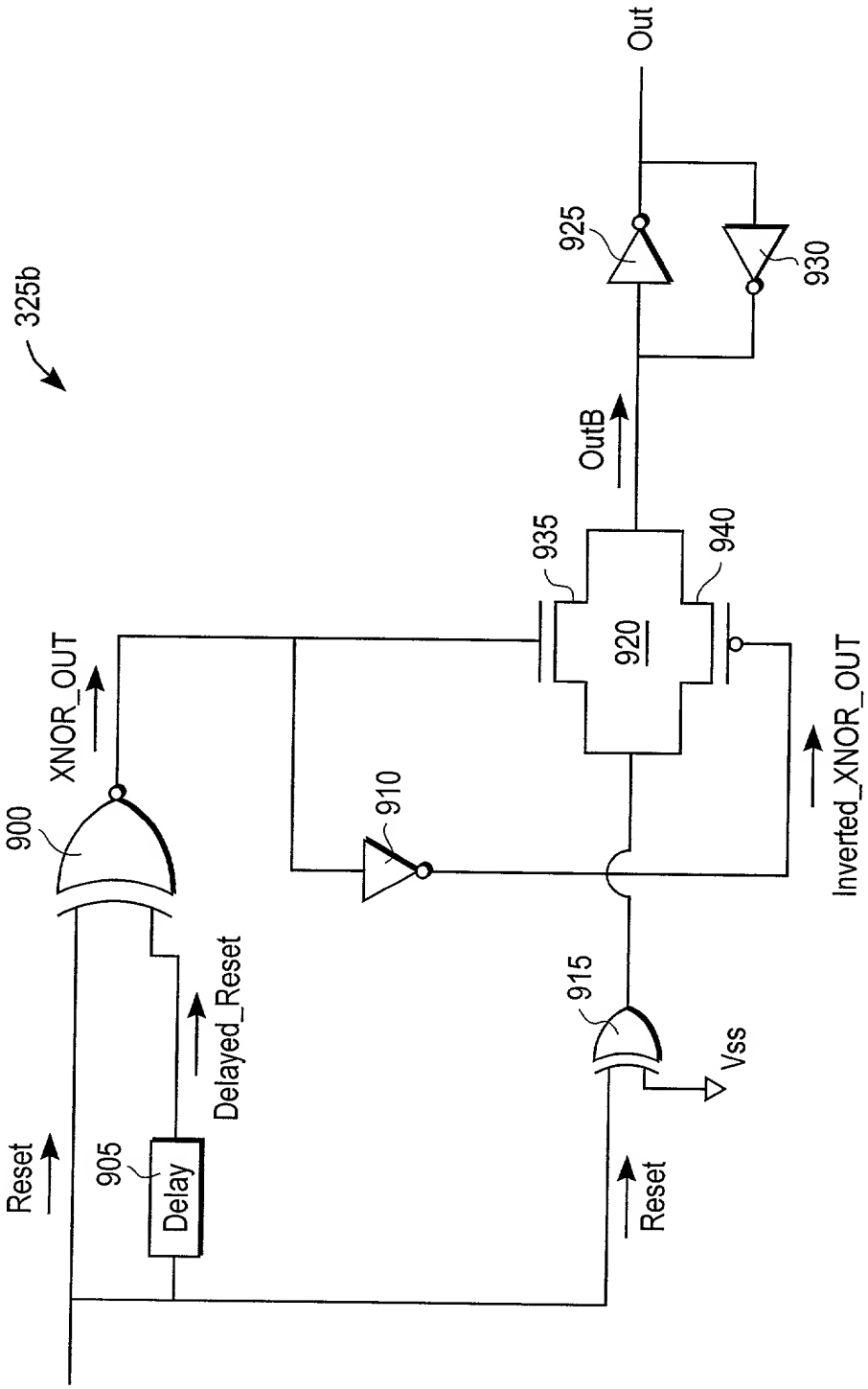


FIG. 9 Sample-Hold Circuit

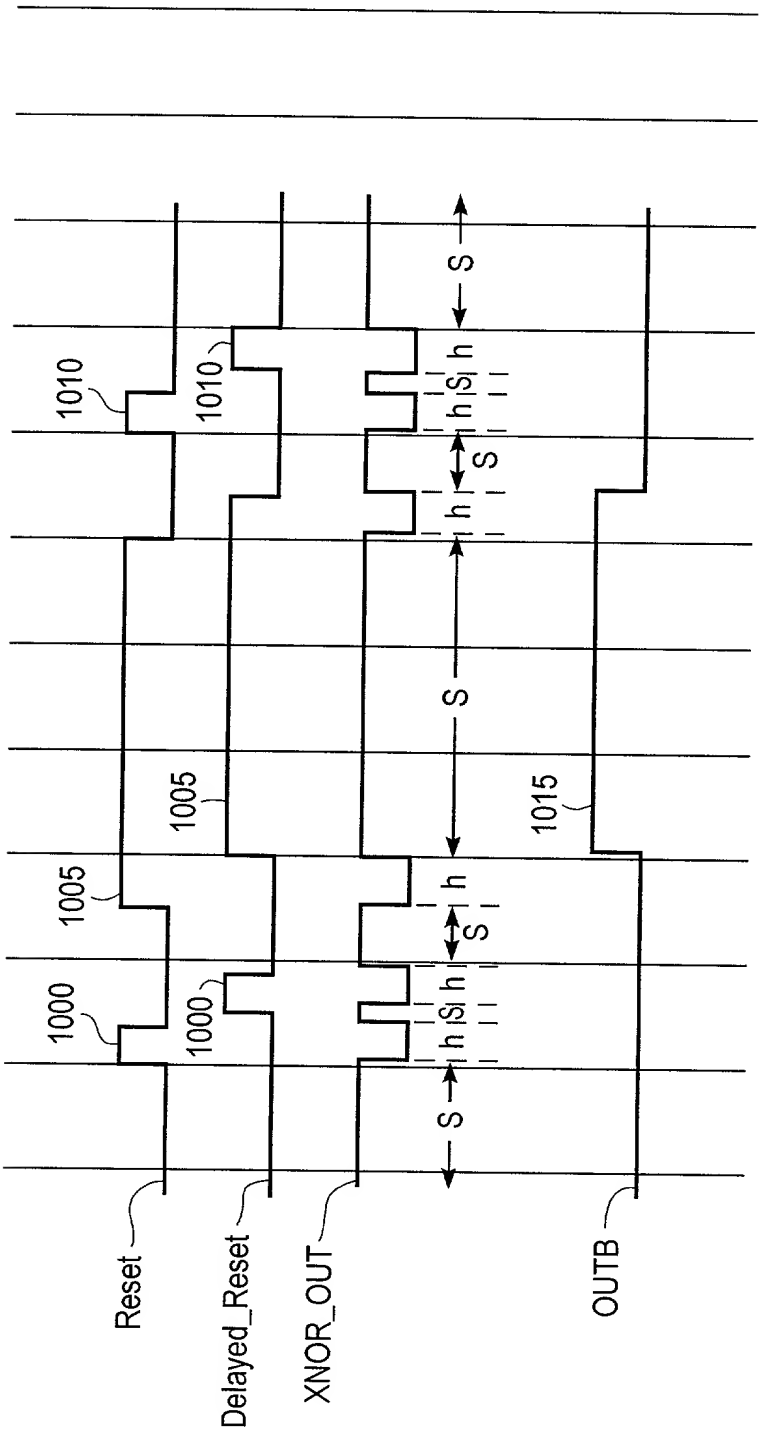


FIG. 10

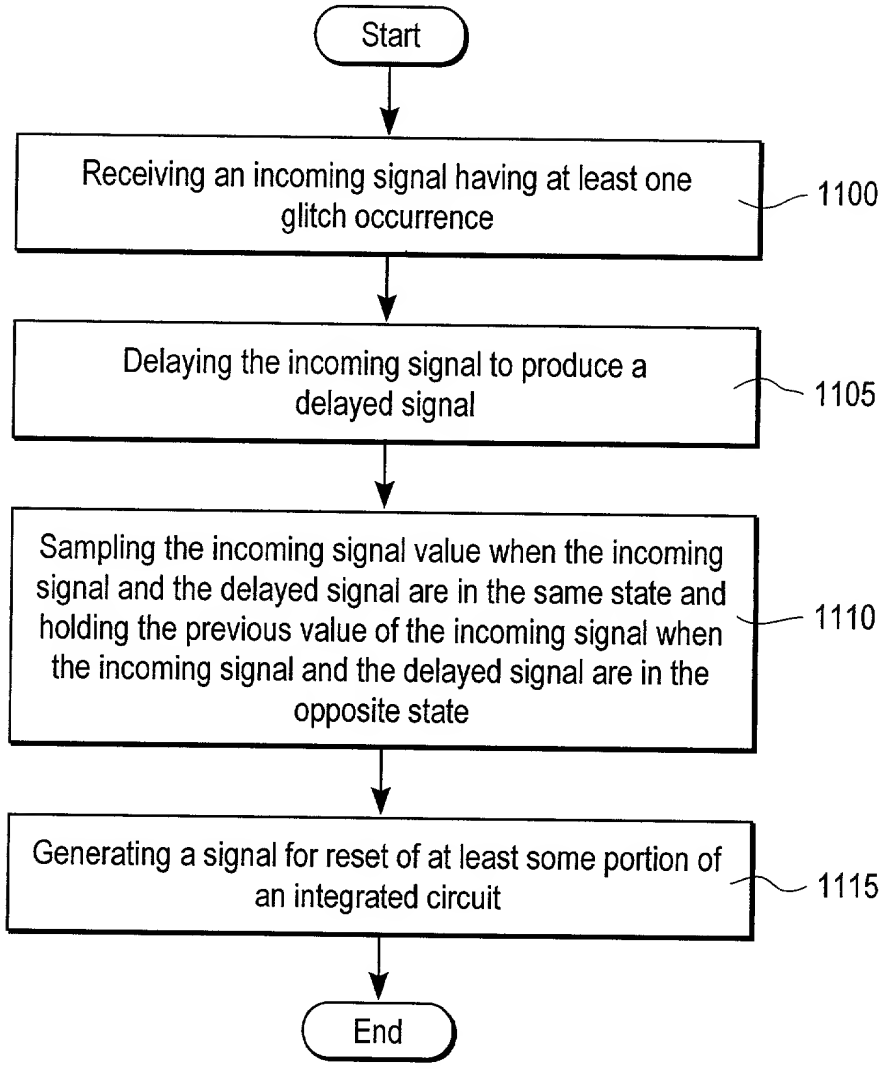


FIG. 11